



1/19

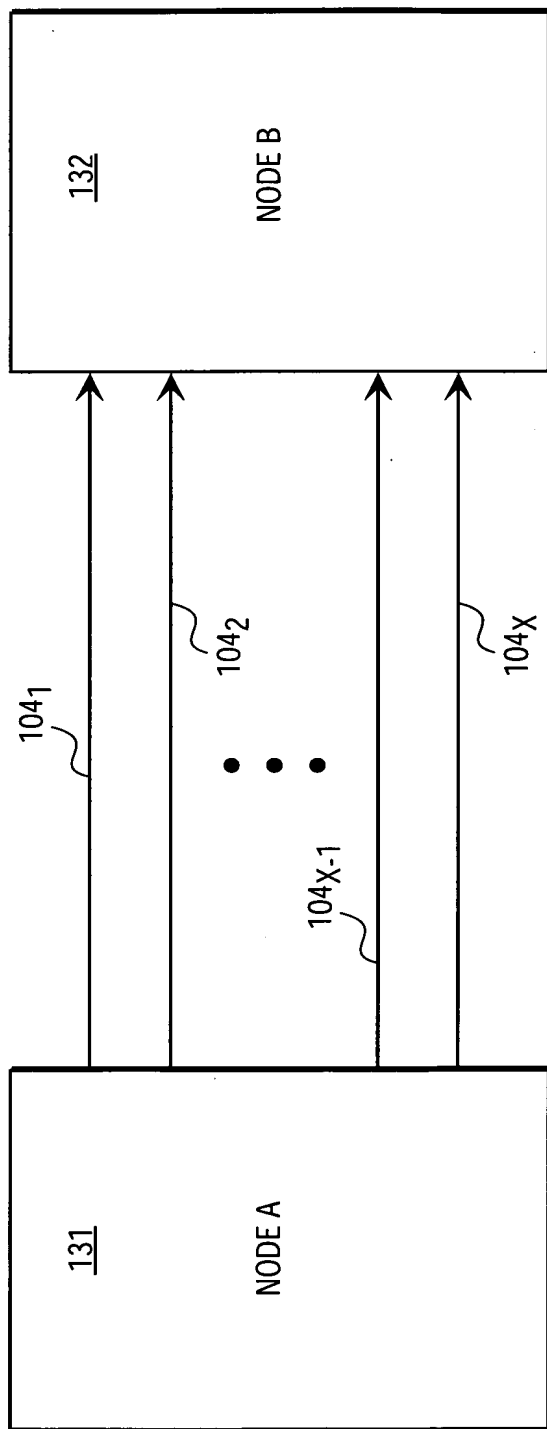


FIG. 1

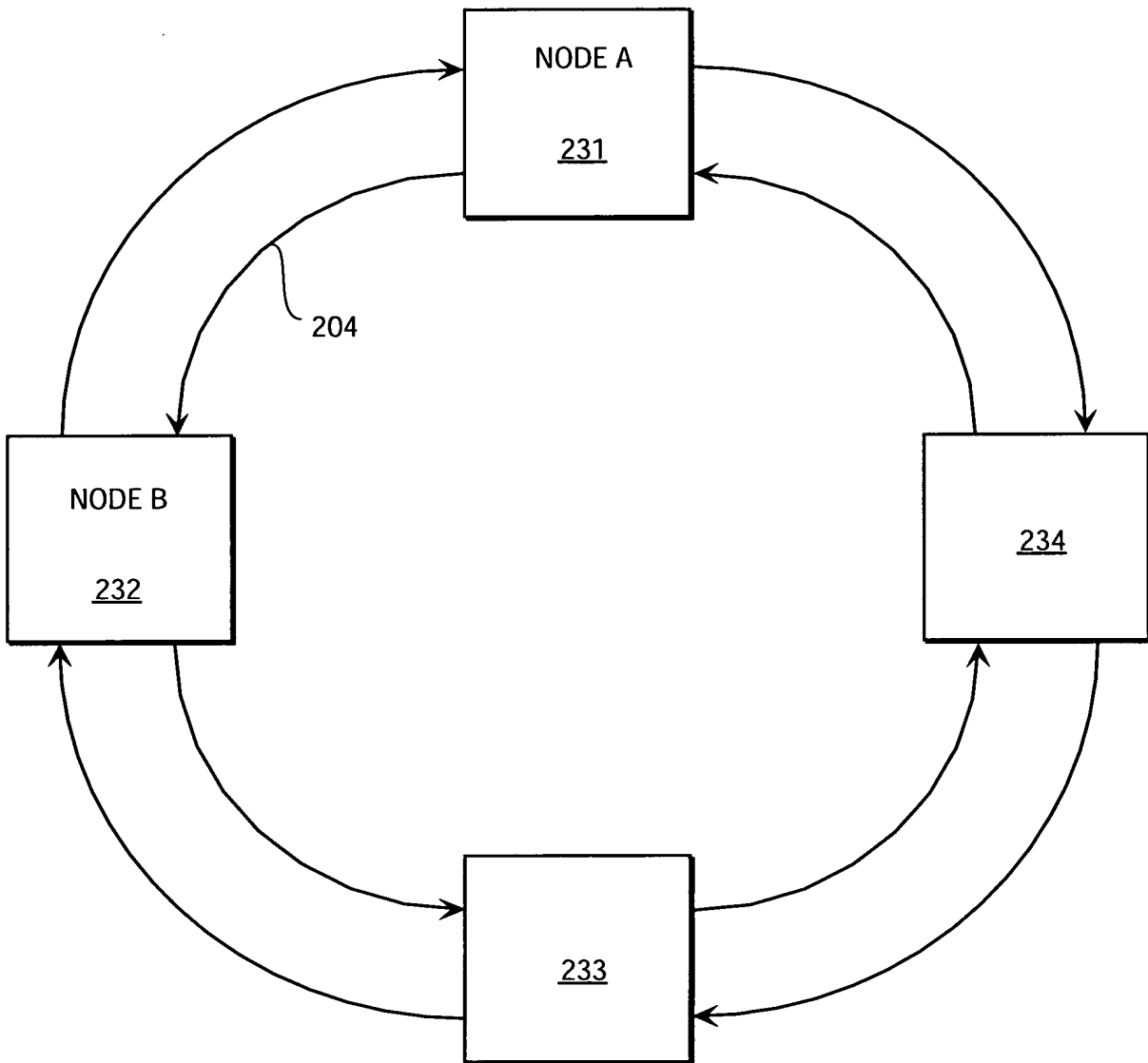


FIG. 2

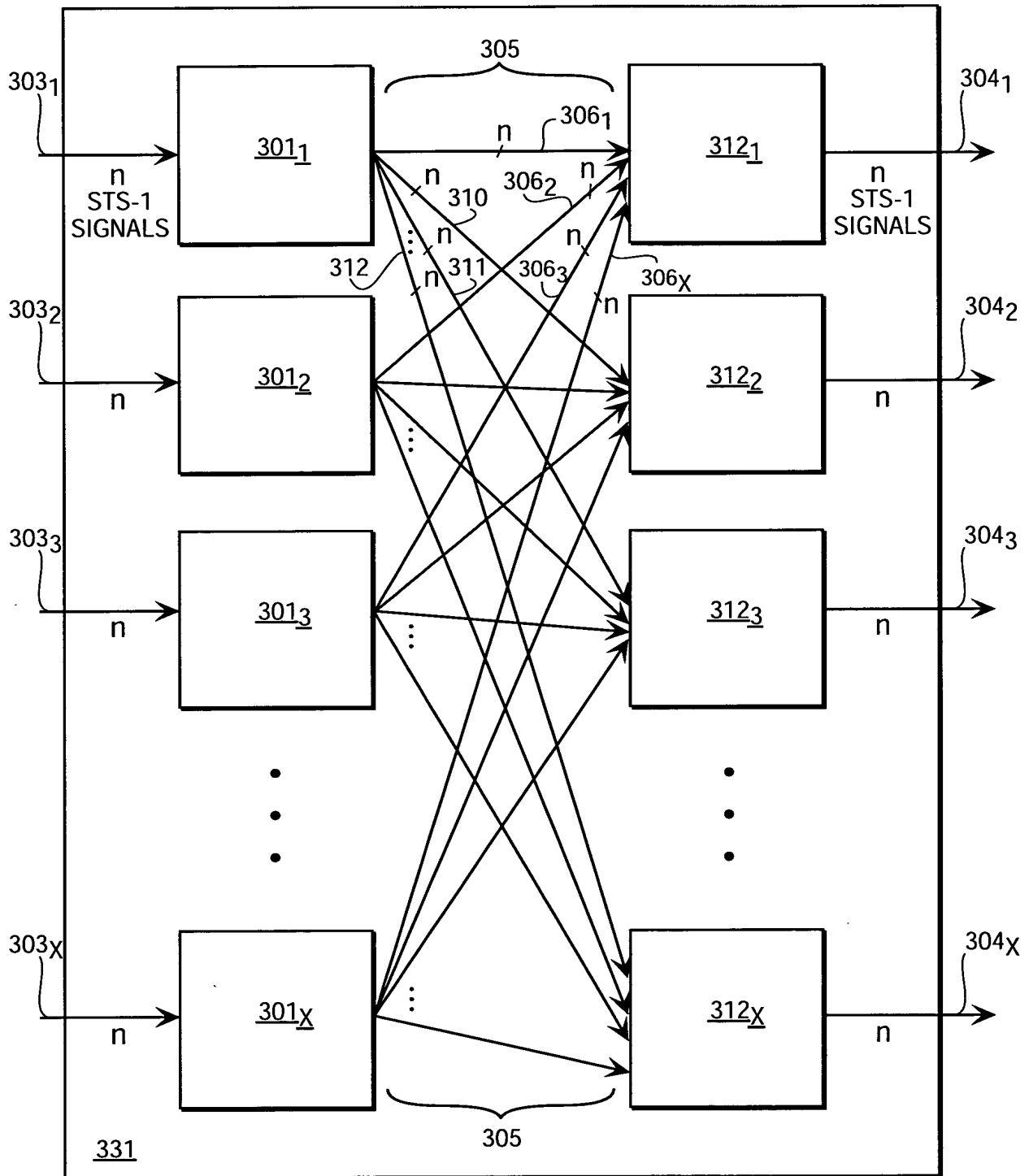


FIG. 3

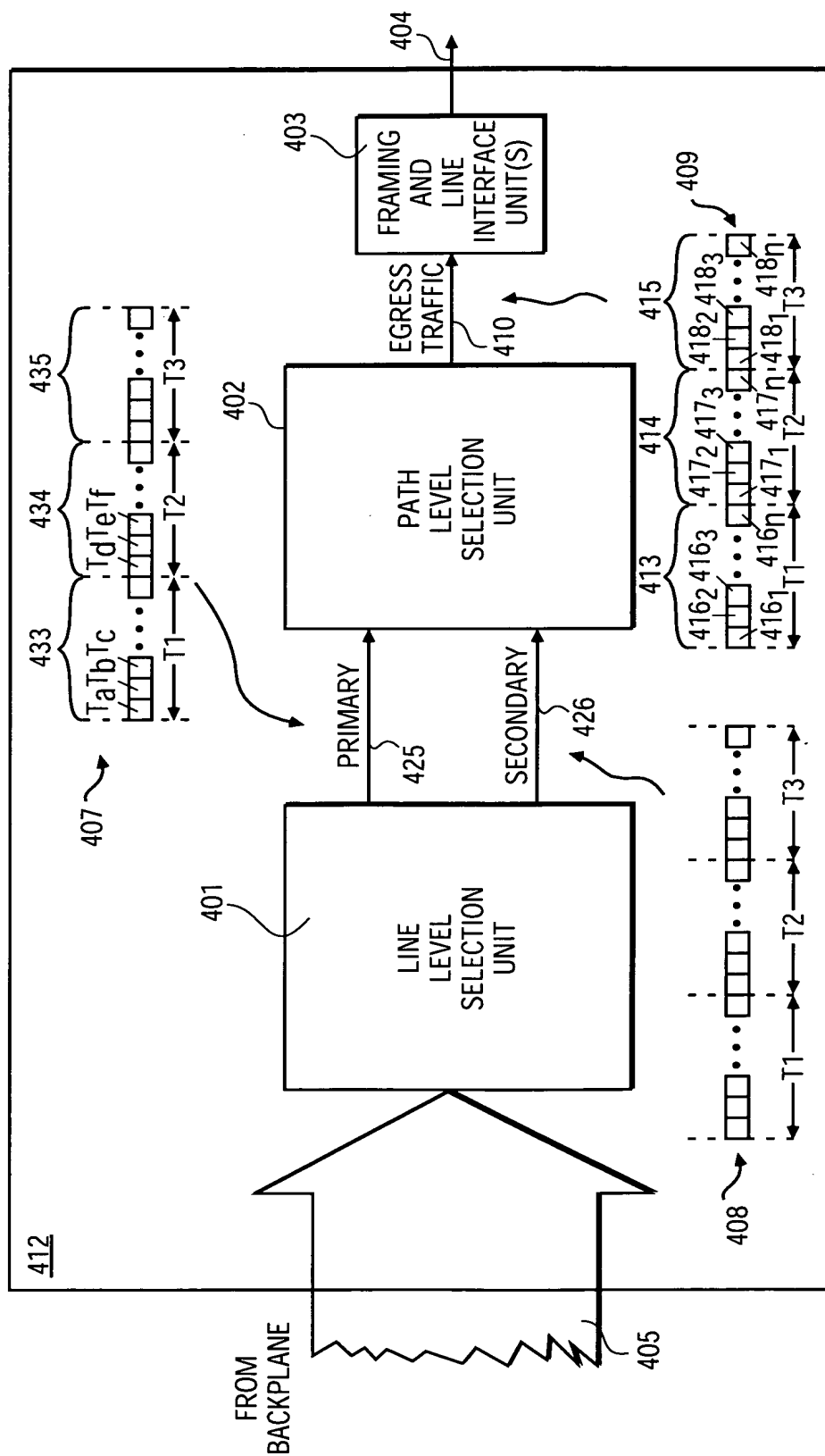


FIG. 4

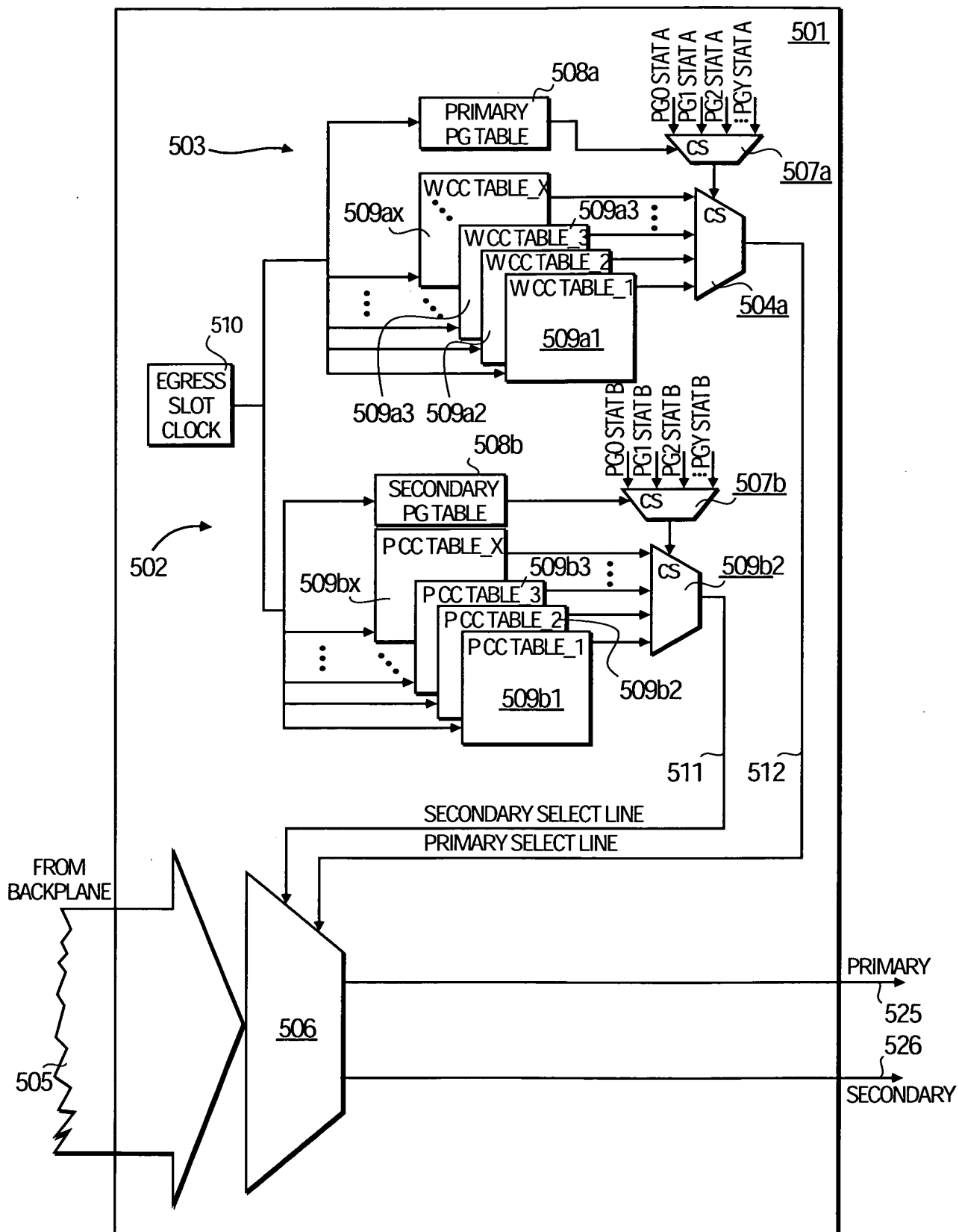


FIG. 5

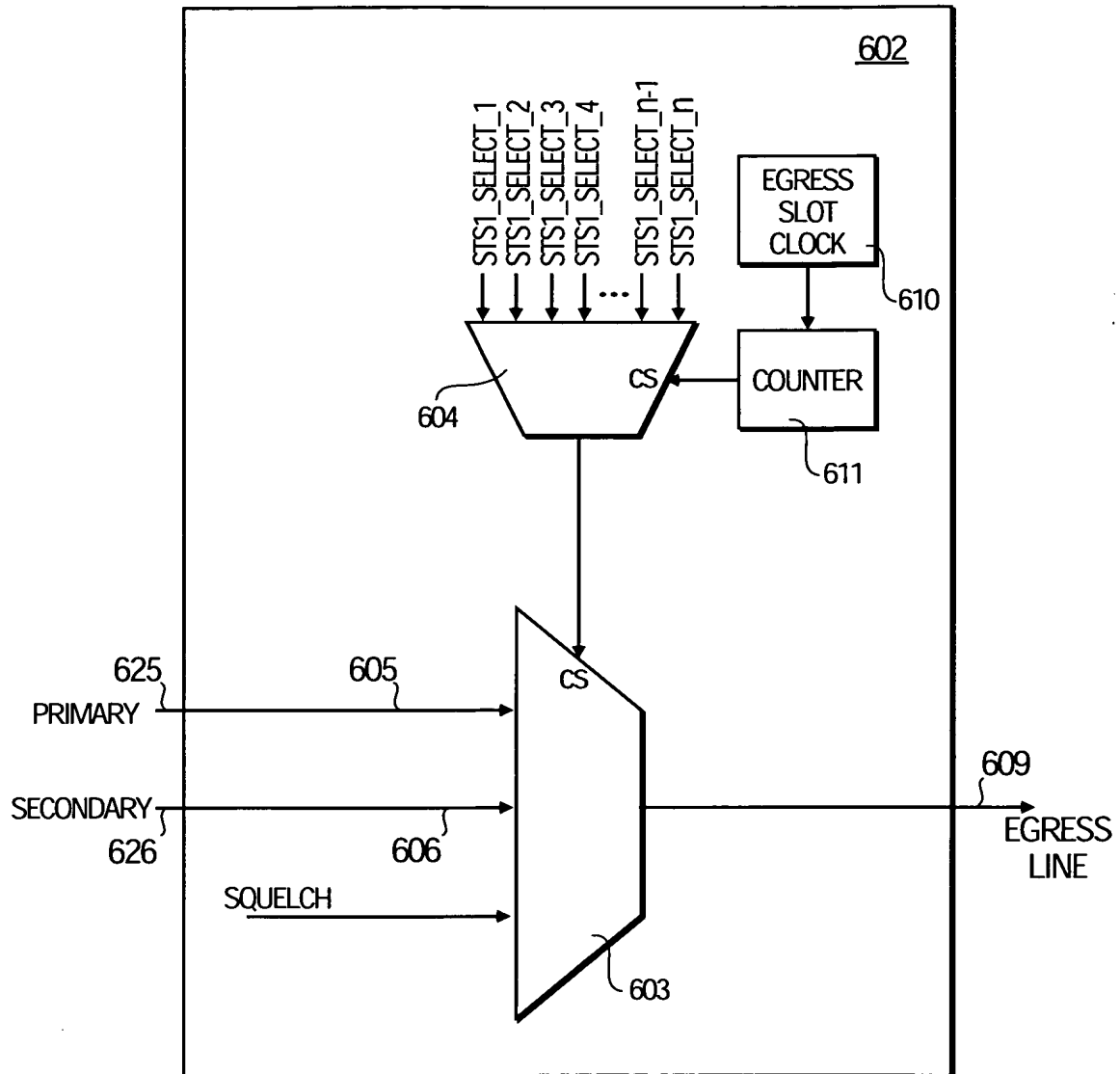


FIG. 6

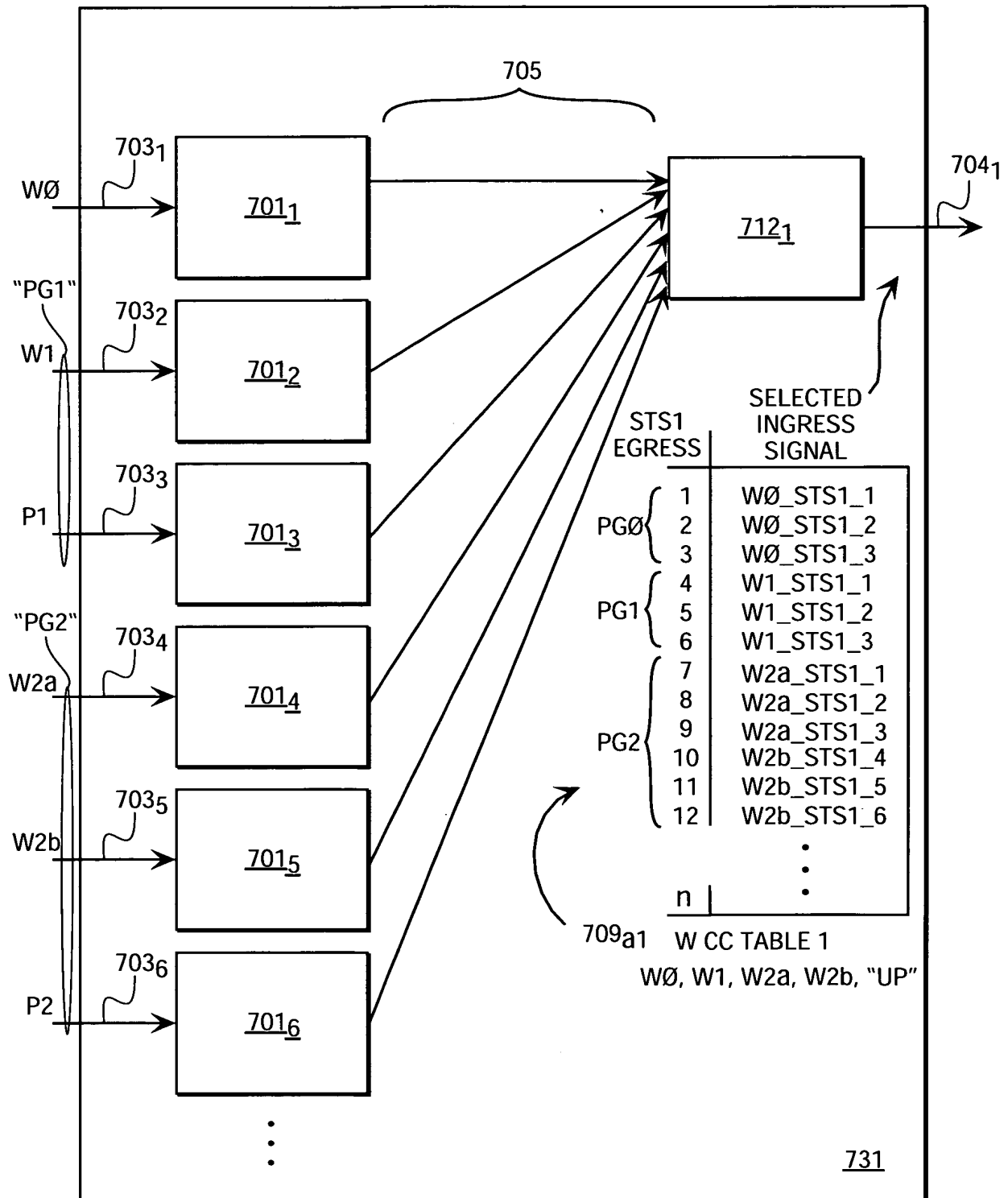


FIG. 7

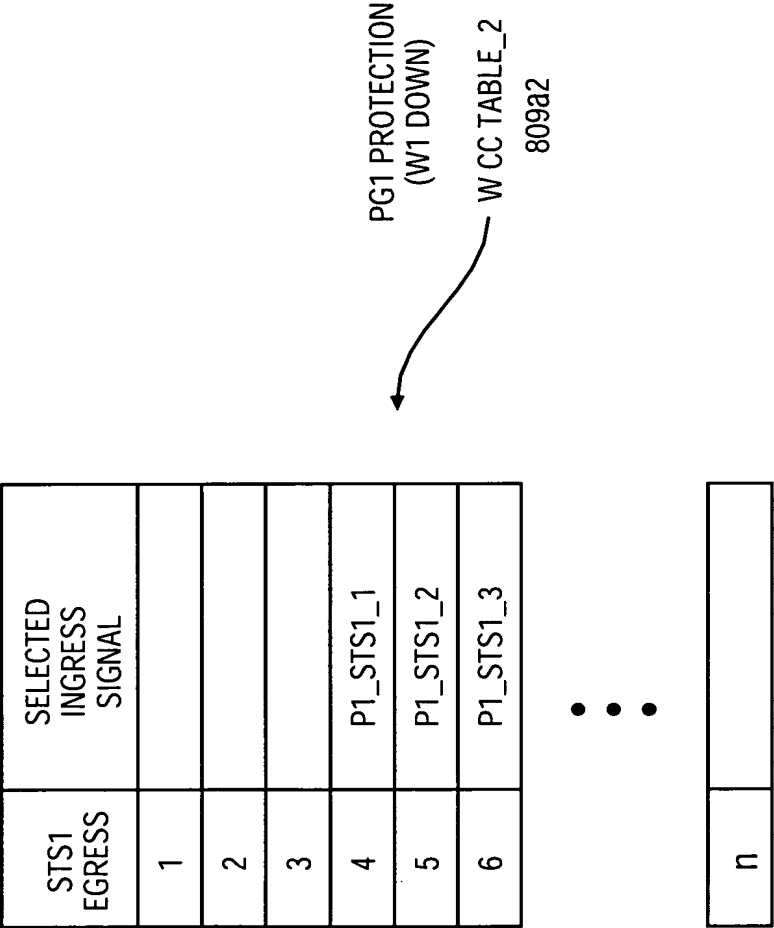


FIG. 8A

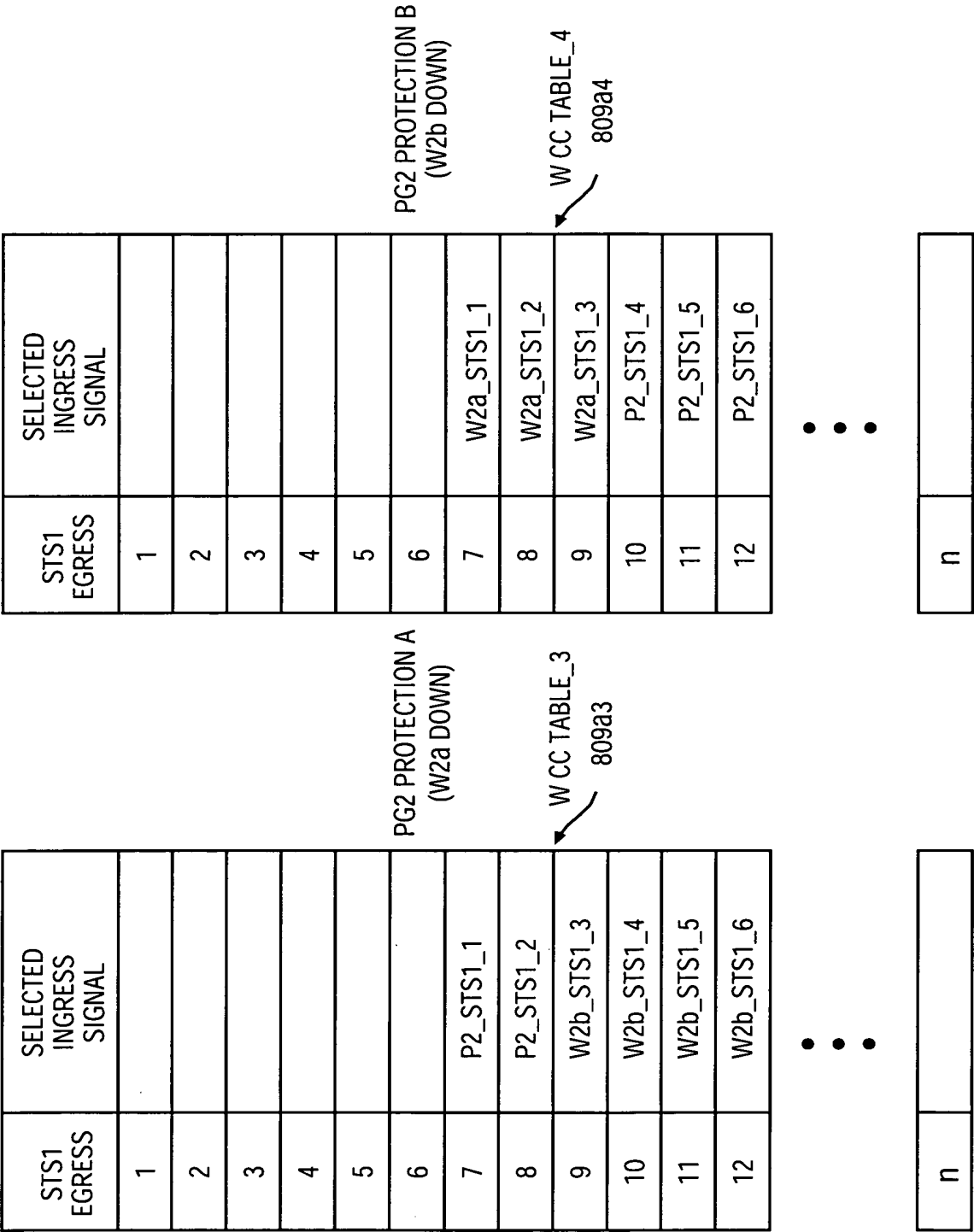


FIG. 8B

STS1 EGRESS	APPLICABLE PROTECTION GROUP
1	PG0
2	PG0
3	PG0
4	PG1
5	PG1
6	PG1
7	PG2
8	PG2
9	PG2
10	PG2
11	PG2
12	PG2
⋮	
n	

PRIMARY PG
TABLE
908a

FIG. 9A

LINE STATUS	PG0 STAT A	PG1 STAT A	PG2 STAT A	
W0, W1, W2a, W2b, "UP"	1	1	1	...
W1 "DOWN"	1	2	1	
W2a "DOWN"	1	1	3	
W2b "DOWN"	1	1	4	
W1 "DOWN", W2a "DOWN"	1	2	3	
W1 "DOWN", W2b "DOWN"	1	2	4	
W1 "DOWN", W2a "DOWN", W2b "DOWN"	1	2	3	
W2a "DOWN", W2b "DOWN"	1	1	3	
	⋮			

FIG. 9B

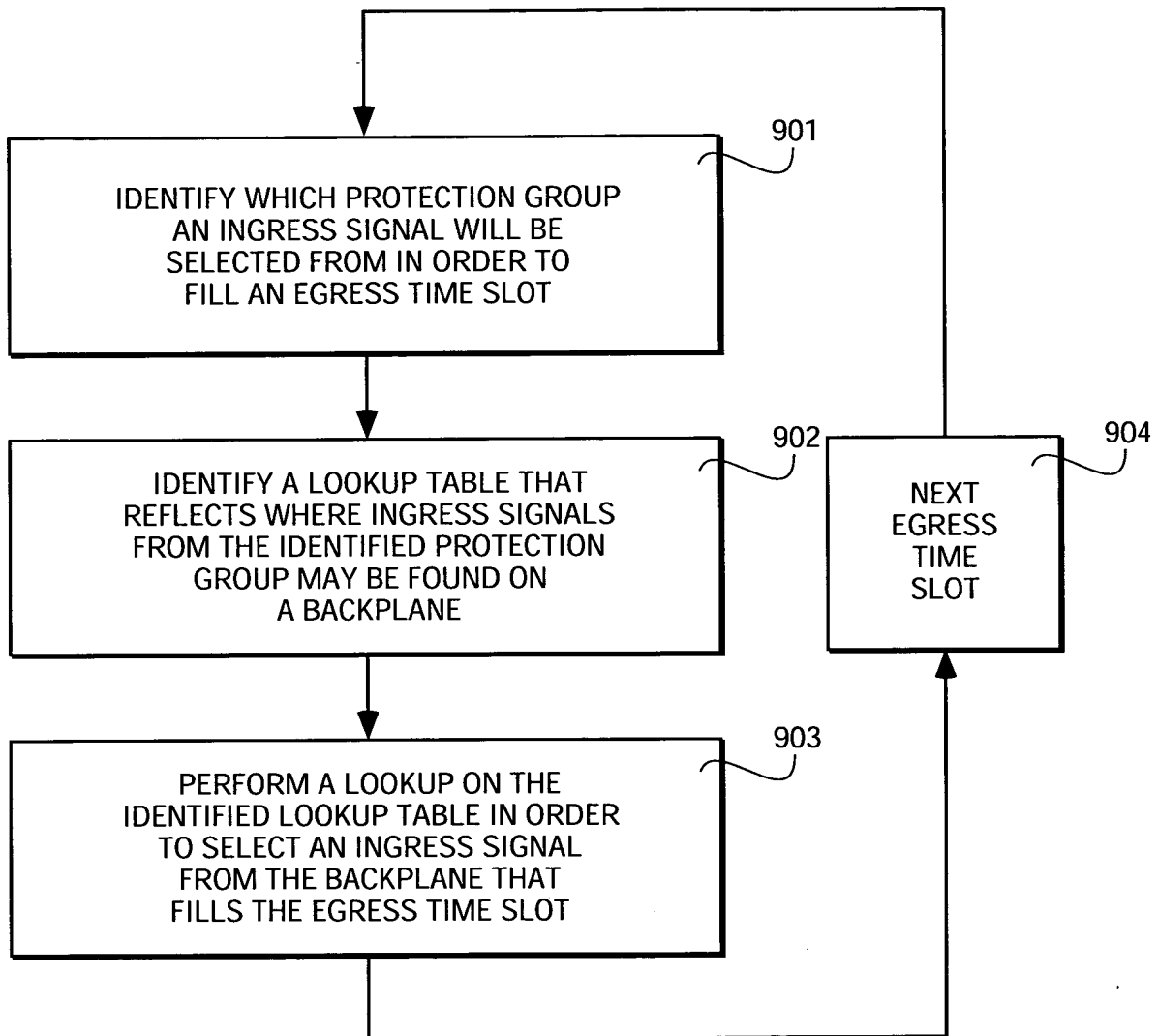


FIG. 9C

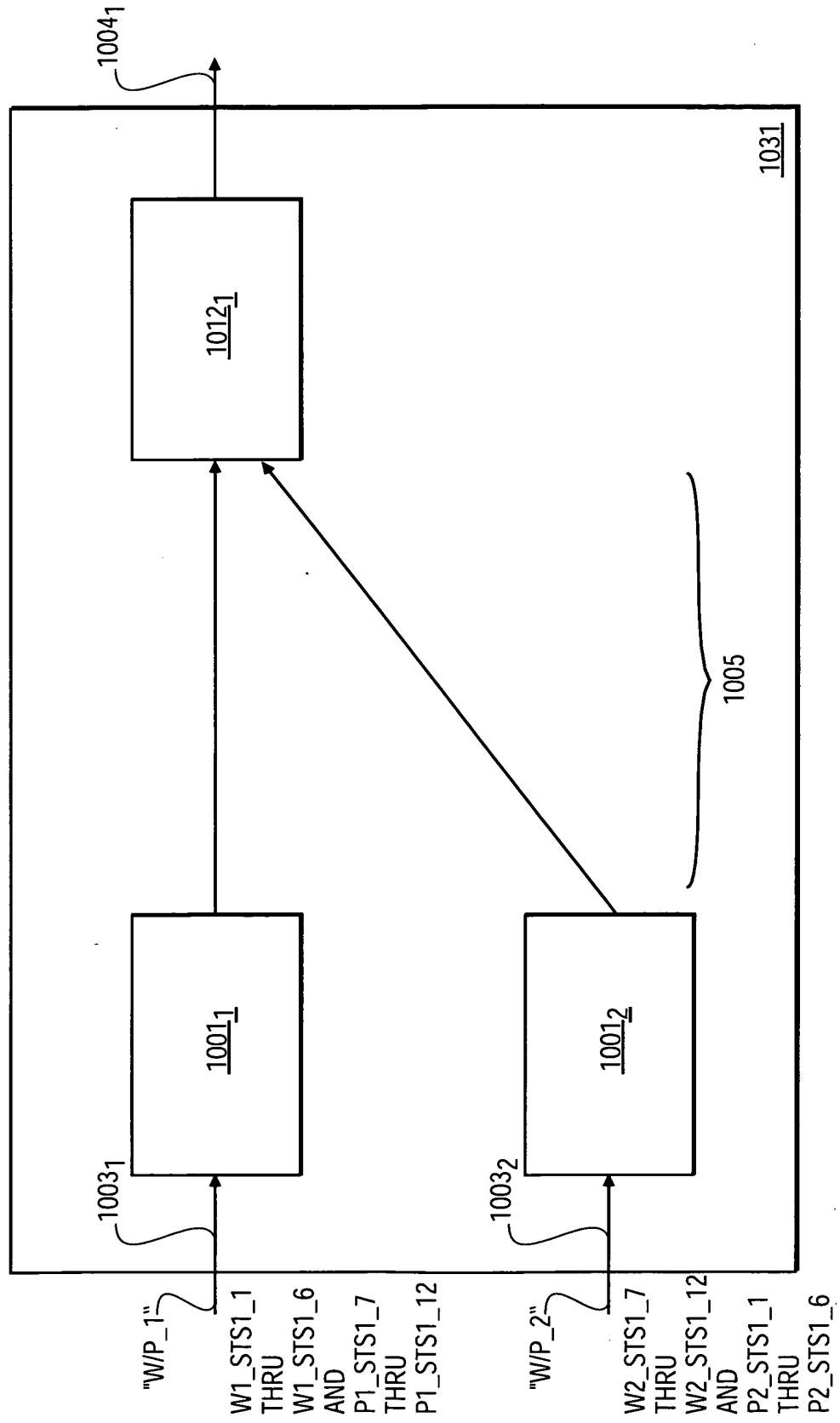
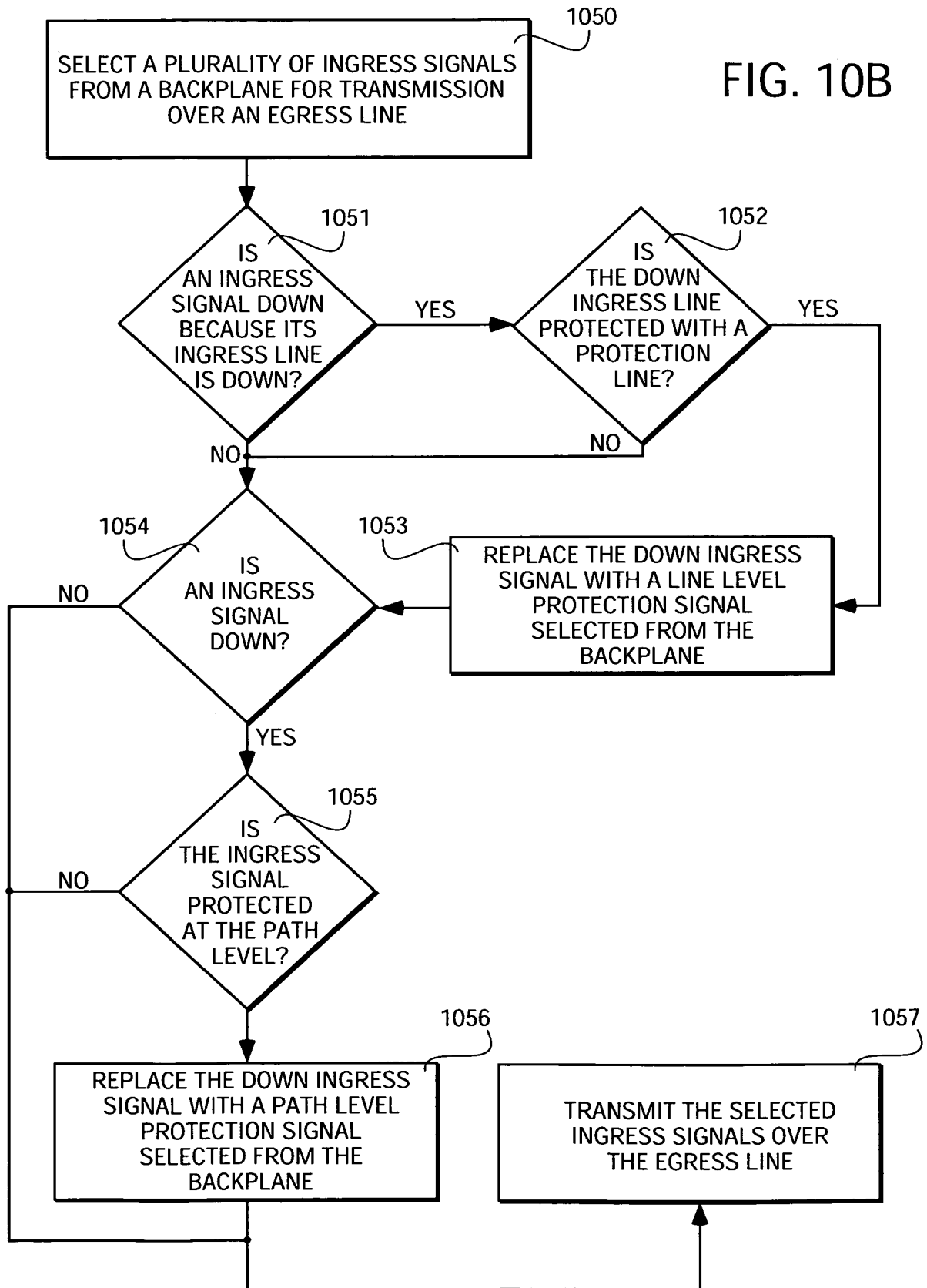


FIG. 10A

FIG. 10B



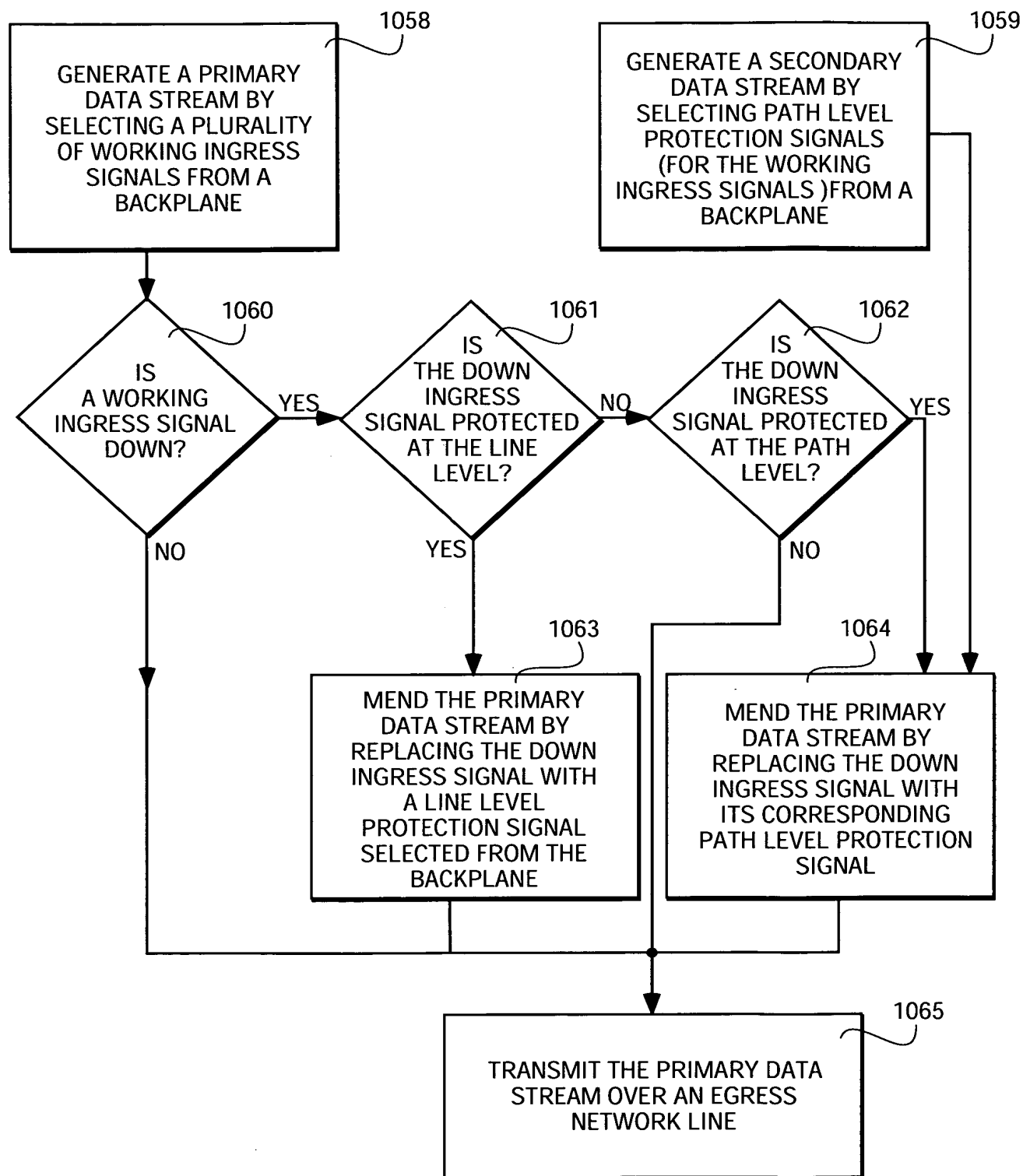


FIG. 10C

STS1 EGRESS	SELECTED INGRESS SIGNAL
1	W1_STS1_1
2	W1_STS1_2
3	W1_STS1_3
4	W1_STS1_4
5	W1_STS1_5
6	W1_STS1_6
7	W2_STS1_7
8	W2_STS1_8
9	W2_STS1_9
10	W2_STS1_10
11	W2_STS1_11
12	W2_STS1_12
	⋮
n	

W CC TABLE_1
1109a1

FIG. 11A

STS1 EGRESS	SELECTED INGRESS SIGNAL
1	P2_STS1_1
2	P2_STS1_2
3	P2_STS1_3
4	P2_STS1_4
5	P2_STS1_5
6	P2_STS1_6
7	P1_STS1_7
8	P1_STS1_8
9	P1_STS1_9
10	P1_STS1_10
11	P1_STS1_11
12	P1_STS1_12
	⋮
n	

P CC TABLE_1
1109b1

FIG. 11B

PATH STATUS	STS1_SELECT_n			...
	STS1_SELECT_n	W	W	W
	...			
	STS1_SELECT_12	W	W	W
	STS1_SELECT_11	W	W	P
	STS1_SELECT_10	W	W	W
	STS1_SELECT_9	W	W	W
	STS1_SELECT_8	W	W	W
	STS1_SELECT_7	W	W	W
	STS1_SELECT_6	W	W	W
	STS1_SELECT_5	W	W	P
	STS1_SELECT_4	W	W	W
	STS1_SELECT_3	W	W	W
	STS1_SELECT_2	W	W	W
	STS1_SELECT_1	W	P	W
	* ALL WORKING PATHS "UP"			
	* W1_STS1_1 "DOWN"			
	* W1_STS1_5 AND W_STS1_11 "DOWN"			

FIG. 12

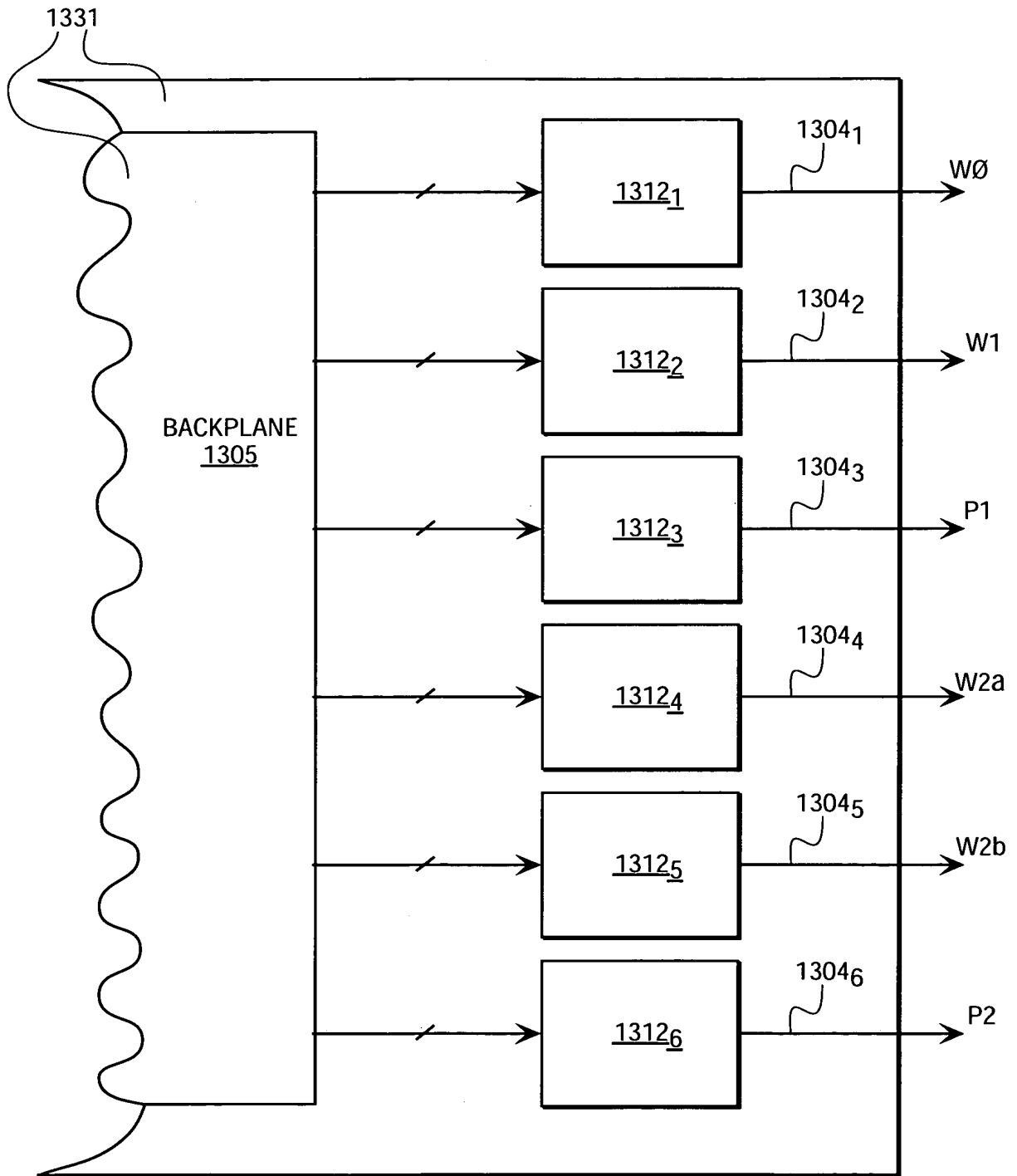


FIG. 13

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2a_STS1_W_1
2	W2a_STS1_W_2
3	W2a_STS1_W_3
4	W2a_STS1_W_4
	⋮
n	W2a_STS1_W_n

W_CC_TABLE_1
1409a1

FIG. 14A

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2a_STS1_P_1
2	W2a_STS1_P_2
3	W2a_STS1_P_3
4	W2a_STS1_P_4
	⋮
n	W2a_STS1_P_n

W_CC_TABLE_2
1409a2

FIG. 14B

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2b_STS1_W_1
2	W2b_STS1_W_2
3	W2b_STS1_W_3
4	W2b_STS1_W_4
	⋮
n	W2b_STS1_W_n

W_CC_TABLE_3
1409a3

FIG. 14C

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2b_STS1_P_1
2	W2b_STS1_P_2
3	W2b_STS1_P_3
4	W2b_STS1_P_4
	⋮
n	W2b_STS1_P_n

W_CC_TABLE_4
1409a4

FIG. 14D

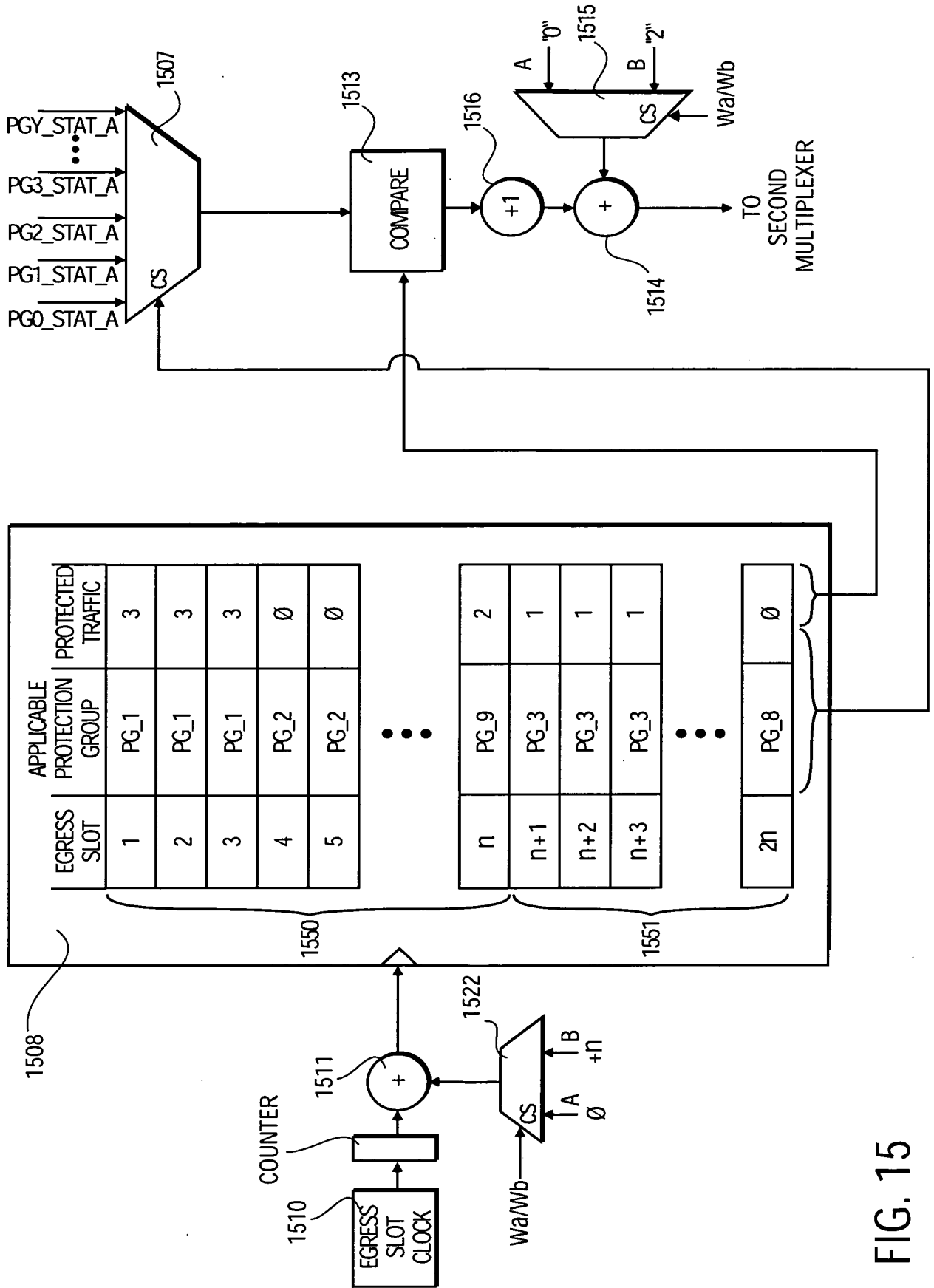


FIG. 15